



U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

**SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT**

Docket Number:
10746/23

Application Number
09/754,632

Filing Date
January 4, 2001

Examiner
Not Yet Assigned

Art Unit
2815

Title
FUNCTION RECONFIGURABLE
SEMICONDUCTOR DEVICE AND
INTEGRATED CIRCUIT CONFIGURING THE
SEMICONDUCTOR DEVICE

Applicant(s)
Kazuo Aoyama et al.

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Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorney(s) for Applicant(s) hereby bring the reference(s) listed on the attached modified PTO Form No. 1449 to the attention of the Examiner. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated.
3. Pursuant to 37 C.F.R. §§ 1.97(b), 1.97(c) and 1.97(e), Applicants' attorney certifies that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign patent application not more than three months prior to the filing of information disclosure statement.
4. While no fee is believed to be due, the Commissioner is authorized (as appropriate or necessary) to charge any fees or credit any overpayment to Deposit Account No. 11-0600. A duplicate copy of this transmittal letter is enclosed for that purpose.

Dated: 7/1/2003

By: 

Aaron C. Deditch (Reg. No. 33,865)

KENYON & KENYON
One Broadway
New York, N.Y. 10004
(212) 425-7200 (telephone)
(212) 425-5288 (facsimile)

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**SUPPLEMENTAL
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STATEMENT BY APPLICANT
PTO-1449 FORM**

ATTY. DOCKET NO.
10746/23

U.S. SERIAL NO.
09/754,632

APPLICANT(S)
Kazuo Aoyama et al.

FILING DATE
January 4, 2001

GROUP
2815

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE*
	5,256,911	October 26, 1993	Mark A. Holler et al.			
	5,305,250	April 19, 1994	Fathi M. A. Salam et al.			
	4,873,661	October 10, 1989	Yannis Tsividis			
	5,343,555	August 30, 1994	Gökce Yayla et al.			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	WO 99/66419*	December 23, 1999				Abst.*	
	EP 1 014 274 A1	June 28, 2000	Europe			English	
	11-194924	July 21, 1999	Japan			Abst.	

*Corresponds to EP 1 014 274 A1 (English).

OTHER DOCUMENTS

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	Czezowski, Peter J. et al. "Fuzzy Neurons: A Coarse-Grained Reconfigurable Element for Computational Intelligence"; Electrical and Computer Engineering, 1999 IEEE Canadian Conference on Edmonton, Alta., Canada 9-12 May 1999, Piscataway, NJ, USA, IEEE, US, 9 May 1999, pp. 1074-1080.
	Horio, Y. et al. "Switched-Capacitor Chaotic Neuron for Chaotic Neural Networks"; Proceedings of the International Symposium on Circuits and Systems. (ISCS). Chicago, May 3-6, 1993, New York, IEEE, US, vol. 2, 3 May 1993, pp. 1018-1021.
	Shibata, Tadashi et al. "FA 15.3: Real-Time Reconfiguration Logic Circuits Using Neuron MOS Transistors"; IEEE International Solid State Circuits Conference, IEEE Inc. New York, US, vol. 36, February 1993, pp. 238-239, 295.

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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